IN THE CLAIMS:

Please cancel claims 1-19 and add new claims 20-25 as follows:

SUBB1

20. (New) In a video data processing apparatus including a controller coupled to a memory circuit that has a plurality of arrays, a method for storing video data in the memory circuit comprising the steps of:

segmenting a plurality of pixels representing one horizontal line of an image display panel into a plurality of pixel groups; and

storing data representing each of said plurality of pixel groups, respectively, in a row of a purality of non-adjoining arrays in the memory circuit.

21. (New) The method of claim 20 further comprising the steps of:

defining tiles of pixels by a two-dimensional grouping of the plurality of pixel groups from a plurality of horizontal lines of the display panel; and

storing data representing each tile of pixels substantially entirely in non-adjoining arrays in the memory circuit.

Sub B2

22 (New) The method of claim 21 wherein the data representing each of said plurality of pixel groups is respectivley stored in the same row of the purality of non-adjoining arrays.

23. (New) The method of claim 22 wherein the data representing each tile of pixels is respectively stored in consecutive rows of the plurality of non-adjoining arrays.